

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	302	717/106.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/23 12:49
L2	343	717/136.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/23 12:49
L3	427	717/140.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/23 12:49
L4	268	717/162.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/23 12:49
L5	9453	(translat\$5 or transform\$5 or (cross adj compil\$5) or cross-compil\$5) and (source adj (files or code)) and (instantiat\$3 or generat\$3 or creat\$3) and (template or framework or pattern)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/23 12:51
L7	53	I1 and I5	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/23 12:51
L8	77	I2 and I5	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/23 12:51
L9	98	I3 and I5	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/23 12:52
L10	22	I4 and I5	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/23 12:52

S1	12	("5836014" "5864700" "5875331" "6026238" "6233731" "6405368"). pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2003/05/05 08:57
S2	132	717/136.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2003/04/10 18:16
S3	325	(translat\$5 or transform\$5 or (cross adj compil\$5) or cross-compiler) and (source adj (files or code)) and (dependency or dependencies) and instantiat\$3 and list and link\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/23 12:50
S4	2	717/136.ccls. and ((translat\$5 or transform\$5 or (cross adj compil\$5) or cross-compiler) and (source adj (files or code)) and (dependency or dependencies) and instantiat\$3 and list and link\$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2003/04/10 18:14
S5	26816	translat\$5 and ((instruction adj set) or (Operating adj system))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2003/04/10 18:16
S6	30	717/136.ccls. and (source adj code) and (object adj code)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2003/04/10 18:35
S7	19	717/136.ccls. and (source adj code) and (object adj code) and target and (target or source or host)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2003/04/10 18:37
S8	136	717/106.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2003/04/24 16:01
S9	10	717/106.ccls. and template near5 code	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2003/04/24 16:11
S10	1210	template near5 code and link\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2003/04/24 16:13

S11	9	((template near5 code) and link\$3 and ((cross near3 compil\$5) or (translat\$3 near3 (instruction adj set)))) not (717/106.ccls. and template near5 code)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2003/04/24 16:18
S12	9	(template near5 code) and link\$3 and ((cross near3 compil\$5) or (translat\$3 near3 (instruction adj set)))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2003/04/29 09:49
S13	9	(template near5 code) and link\$3 and ((cross near3 compil\$5) or (translat\$3 near3 (instruction adj set)) or cross-compil\$5)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2003/04/29 09:48
S14	1159	(template near3 code)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2003/04/29 09:48
S15	651	(template near5 code) and link\$3 and ((cross near3 compil\$5) or (translat\$3) or cross-compil\$5)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2003/04/29 16:49
S16	10	((template adj source) adj code) and link\$3 and ((cross near3 compil\$5) or (translat\$3) or cross-compil\$5)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2003/04/29 09:51
S17	3	"6233731".pn. or "6026238".pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2003/04/29 16:34
S18	11	("5390314" "5406644" "5680551" "5687376" "5715459" "5729748" "5754858" "5764947" "5910180" "6026238" "6052527").PN.	USPAT	OR	OFF	2003/04/29 15:49
S19	1	"6233731".URPN.	USPAT	OR	OFF	2003/04/29 15:57
S20	5	("5390314" "5680551" "5729748" "5754858" "5764947").PN.	USPAT	OR	OFF	2003/04/29 16:01
S21	4	"6026238".URPN.	USPAT	OR	OFF	2003/04/29 16:07
S22	321	(amend\$3 or chang\$3 or alter\$3) near3 (source adj code) and dependen\$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2003/04/30 17:25

S23	3	(amend\$3 or chang\$3 or alter\$3) near3 (source adj code) and dependen\$4 and 717/136.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2003/04/29 16:41
S24	182	(edit\$3 or amend\$3 or chang\$3 or alter\$3) near3 (source adj code) and dependen\$4 and 717/???ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2003/04/29 16:43
S25	5	(edit\$3 or amend\$3 or chang\$3 or alter\$3) near3 (source adj code) and dependen\$4 and 717/137.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2003/04/29 16:43
S26	128	(template near5 code) and link\$3 and ((cross near3 compil\$5) or (translat\$3) or cross-compil\$5) and instantiat\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2003/04/29 16:50
S27	35	(template near5 code) and link\$3 and ((cross near3 compil\$5) or (translat\$3) or cross-compil\$5) and instantiat\$3 and 717/???ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2003/04/29 16:51
S28	1	"5836014".pn. and link\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2003/04/30 10:08
S29	2	(amend\$3 or chang\$3 or alter\$3) near3 (template near2 (source adj code))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2003/04/30 17:32
S30	9	compil\$5 near3 (template near2 (source adj code))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2003/04/30 17:37
S31	31	(binary adj translat\$3) and (template or framwwork or macro or "parameterized type" or pattern) and (source and object) near2 (code or instruction)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/17 07:09
S32	31	(binary adj translat\$3) and (template or framwwork or macro or "parameterized type" or pattern) and (source and object) near2 (code or instruction)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/17 07:44

S33	3187	(template or framwwork or macro or "parameterized type" or pattern) and (source and object) near2 (code or instruction)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/17 07:45
S34	21343	source near3 (template or framwwork or macro or "parameterized type" or pattern)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/17 07:46
S35	63	"template source code"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/17 07:47
S36	33	("4827404" "5075851" "5159687" "5187788" "5339433" "5361350" "5481708" "5493680" "5497319" "5535120").PN. OR ("5675801").URPN.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/05/17 07:58
S37	33	((("4827404" "5075851" "5159687" "5187788" "5339433" "5361350" "5481708" "5493680" "5497319" "5535120").PN. OR ("5675801").URPN.) and (source object template framework macro parameterized compil\$5 translat\$3)	US-PGPUB; USPAT; USOCR	OR	OFF	2005/05/17 07:59
S38	28	("4734854" "4819233" "4866610" "5159687" "5163130" "5295222" "5414847" "5428792" "5493680" "5499371" "5526522" "5557730" "5560010" "5623657" "5640558" "5675801" "5694598" "5699310" "5732257" "5737597" "5754858" "5819281" "5845289" "5875333" "5903756" "5923878" "6085198" "6237136").PN. OR ("6742175").URPN.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/05/17 08:15

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S40	301559	("source to source" or (source near\$ source)) and (source object template framework macro parameterized compil\$5 transl\$3)	US-PGPUB; USPAT; USOCR	OR	OFF	2005/05/17 09:47
S41	16	("source to source" or (source near\$ source)) near\$ (translat\$3) and (translat\$3 near\$ (template or framework or parameterized)) and (source object template framework macro parameterized compil\$5 transl\$3)	US-PGPUB; USPAT; USOCR	OR	OFF	2005/05/17 09:52
S42	8	("source to source" or (source near\$ source)) near\$ (translat\$3) and (translat\$3 near\$ (template or framework or parameterized)) and (source object template framework macro parameterized compil\$5 transl\$3) and (source near2 (template or framework or parameterized))	US-PGPUB; USPAT; USOCR	OR	OFF	2005/05/17 09:53
S43	3	("5317676").URPN.	USPAT	OR	OFF	2005/05/17 10:00
S44	8	("5613117" "5623617" "5659753" "5828875" "5911070" "6016467" "6237135").PN. OR ("6519768").URPN.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/05/17 10:09
S45	6	"parameterized source code"	US-PGPUB; USPAT; USOCR	OR	OFF	2005/05/17 10:15
S46	1	"templated source code"	US-PGPUB; USPAT; USOCR	OR	OFF	2005/05/17 10:16

S47	582	((creat\$3 or generat\$3 or instantiat\$3) near3 template) near5 source	US-PGPUB; USPAT; USOCR	OR	ON	2005/05/17 10:17
S48	423	S47 and S34	US-PGPUB; USPAT; USOCR	OR	ON	2005/05/17 10:17
S49	52	S47 and S34 and S33	US-PGPUB; USPAT; USOCR	OR	ON	2005/05/17 10:17
S50	9	("5715460" "5864700" "5911076" "5956725" "5978585" "6141792" "6317871").PN. OR ("6405368"). URPN.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/05/17 10:34


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1 [Technical correspondence: Templates-based portable just-in-time compiler](#)

Alex Iliasov

 August 2003 **ACM SIGPLAN Notices**, Volume 38 Issue 8

 Full text available: [pdf\(259.53 KB\)](#)

 Additional Information: [full citation](#), [abstract](#), [references](#)

Usage of the platform-neutral bytecode interpreters is often limited by their restricted performance. Just-in-time compilers effectively solve this problem. However they are hard to develop and retarget. This paper demonstrates that dynamic code generation from the templates created by a C compiler can be used to build a simple and highly-portable JIT compiler.

Keywords: bytecode interpretation, code, dynamic code generation, inlining, just-in-time compilation, templates

2 [Impact of economics on compiler optimization](#)

Arch D. Robison

 June 2001 **Proceedings of the 2001 joint ACM-ISCOPE conference on Java Grande**

 Full text available: [pdf\(764.92 KB\)](#)

 Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

Compile-time program optimizations are similar to poetry: more are written than are actually published in commercial compilers. Hard economic reality is that many interesting optimizations have too narrow an audience to justify their cost in a general-purpose compiler, and custom compilers are too expensive to write. An alternative is to allow programmers to define their own compile-time optimizations. This has already happened accidentally for C++, albeit imperfectly, in the form of template ...

Keywords: compilers, economics, optimization

3 [Novel self-test methods: A scalable software-based self-test methodology for programmable processors](#)

Li Chen, Srivaths Ravi, Anand Raghunathan, Sujit Dey

June 2003 **Proceedings of the 40th conference on Design automation**

Full text available: [pdf\(107.71 KB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

Software-based self-test (SBST) is an emerging approach to address the challenges of high-quality, at-speed test for complex programmable processors and systems-on chips (SoCs) that contain them. While early work on SBST has proposed several promising ideas, many challenges remain in applying SBST to realistic embedded processors. We propose a systematic scalable methodology for SBST that automates several key steps. The proposed methodology consists of (i) identifying test program templates tha ...

Keywords: at-speed test, manufacturing test, microprocessor, scalability, software-based self-test, test program

4 [PLI workshops: Template meta-programming for Haskell](#)

Tim Sheard, Simon Peyton Jones

December 2002 **ACM SIGPLAN Notices**, Volume 37 Issue 12

Full text available: [pdf\(244.61 KB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

We propose a new extension to the purely functional programming language Haskell that supports *compile-time meta-programming*. The purpose of the system is to support the *algorithmic* construction of programs at compile-time. The ability to generate code at compile time allows the programmer to implement such features as polytypic programs, macro-like expansion, user directed optimization (such as inlining), and the generation of supporting data structures and functions from existing ...

Keywords: Meta programming, templates

5 [Template meta-programming for Haskell](#)

Tim Sheard, Simon Peyton Jones

October 2002 **Proceedings of the ACM SIGPLAN workshop on Haskell**

Full text available: [pdf\(169.20 KB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

We propose a new extension to the purely functional programming language Haskell that supports *compile-time meta-programming*. The purpose of the system is to support the *algorithmic* construction of programs at compile-time. The ability to generate code at compile time allows the programmer to implement such features as polytypic programs, macro-like expansion, user directed optimization (such as inlining), and the generation of supporting data structures and functions from existing ...


Keywords: meta programming, templates

6 [SYZYGY - A Framework for Scalable Cross-Module IPO](#)

Sungdo Moon, Xinliang D. Li, Robert Hundt, Dhruva R. Chakrabarti, Luis A. Lozano, Uma Srinivasan, Shin-Ming Liu

March 2004 **Proceedings of the international symposium on Code generation and optimization:**

feedback-directed and runtime optimization


Full text available:  [pdf\(198.14 KB\)](#) Additional Information: [full citation](#), [abstract](#)

Performing analysis across module boundaries for an entire program is important for exploiting several runtime performance opportunities. However, due to scalability problems in existing full-program analysis frameworks, such performance opportunities are only realized by paying tremendous compile-time costs. Alternative solutions, such as partial compilations or user assertions, are complicated or unsafe and as a result, not many commercial applications are compiled today with cross-module optimizations. ...

7 [Next-generation generic programming and its application to sparse matrix computations](#)

Nikolay Mateev, Keshav Pingali, Paul Stodghill, Vladimir Kotlyar

May 2000 **Proceedings of the 14th international conference on Supercomputing**

Full text available:  [pdf\(1.06 MB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

The contributions of this paper are the following. We introduce a new variety of generic programming in which algorithm implementors use a different API than data structure designers, the gap between the API's being bridged by restructuring compilers. One view of this approach is that it exploits restructuring compiler technology to perform a novel kind of template instantiation. We demonstrate the usefulness of this new generic programming technology ...

8 [\(ISEF\): an integrated industrial-strength software engineering framework](#)

Shaye Koenig

November 1988 **Proceedings of the third ACM SIGSOFT/SIGPLAN software engineering symposium on Practical software development environments**, Volume 13, 24 Issue 5, 2



Full text available:  [pdf\(1.34 MB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

ISEF is an environment for programming-in-the-large that integrates disparate software engineering principles, methods and tools into an industrial-strength, automated software development framework. Projects using ISEF have reported increased software quality, improved software manageability and decreased software production costs. This paper presents the basic principles and mechanisms that enable ISEF to achieve environment/process integration as well as integration within the environment ...

9 [Composition patterns: an approach to designing reusable aspects](#)

Siobhán Clarke, Robert J. Walker


July 2001 **Proceedings of the 23rd International Conference on Software Engineering**

Full text available:  [pdf\(187.98 KB\)](#)  Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)
[Publisher Site](#)

Requirements such as distribution or tracing have an impact on multiple classes in a system. They are *cross-cutting* requirements, or *aspects*. Their support is, by necessity, scattered across those multiple classes. A look at an individual class may also show support for cross-cutting requirements tangled up with the core responsibilities of that class. Scattering and tangling make object-oriented software difficult to understand, extend and reuse. Though design is an important a ...

10 Session S8.2: system synthesis: Instruction generation and regularity extraction for reconfigurable processors

Philip Brisk, Adam Kaplan, Ryan Kastner, Majid Sarrafzadeh


October 2002 **Proceedings of the 2002 international conference on Compilers, architecture, and synthesis for embedded systems**Full text available:  [pdf\(203.26 KB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

The increasing demand for complex and specialized embedded hardware must be met by processors which are optimized for performance, yet are also extremely flexible. In our work, we explore the tradeoff between flexibility and performance in the domain of reconfigurable processor design. Specifically, we seek to identify regularly occurring, computation-heavy patterns in an application or set of applications. These patterns become candidates for hard-logic implementation, potentially embedded in t ...

Keywords: control data-flow graph, hardware compiler, slack, template

11 A comparative analysis of schemes for correlated branch prediction


Cliff Young, Nicolas Gloy, Michael D. Smith

May 1995 **ACM SIGARCH Computer Architecture News , Proceedings of the 22nd annual international symposium on Computer architecture**, Volume 23 Issue 2Full text available:  [pdf\(1.50 MB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

Modern high-performance architectures require extremely accurate branch prediction to overcome the performance limitations of conditional branches. We present a framework that categorizes branch prediction schemes by the way in which they partition dynamic branches and by the kind of predictor that they use. The framework allows us to compare and contrast branch prediction schemes, and to analyze why they work. We use the framework to show how a static correlated branch prediction scheme increas ...

12 Code size minimization and retargetable assembly for custom EPIC and VLIW instruction formats

Shail Aditya, Scott A. Mahlke, B. Ramakrishna Rau


October 2000 **ACM Transactions on Design Automation of Electronic Systems (TODAES)**, Volume 5 Issue 4Full text available:  [pdf\(568.33 KB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

PICO is a fully automated system for designing the architecture and the microarchitecture of VLIW and EPIC processors. A serious concern with this class of processors, due to their very long instructions, is their code size. One focus of this paper is to describe a series of code size minimization techniques used within PICO, some of which are applied during the automatic design of the instruction format, while others are applied during program assembly. The design of a retargetable assembl ...

Keywords: EPIC, VLIW, code size minimization, custom templates, design automation, instruction format design, noop compression, retargetable assembly

13 Call graph construction in object-oriented languages


David Grove, Greg DeFouw, Jeffrey Dean, Craig Chambers

October 1997 **ACM SIGPLAN Notices , Proceedings of the 12th ACM SIGPLAN conference on Object-oriented programming, systems, languages, and applications, Volume 32 Issue 10**Full text available:  [pdf\(2.63 MB\)](#)Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)


Interprocedural analyses enable optimizing compilers to more precisely model the effects of non-inlined procedure calls, potentially resulting in substantial increases in application performance. Applying interprocedural analysis to programs written in object-oriented or functional languages is complicated by the difficulty of constructing an accurate program call graph. This paper presents a parameterized algorithmic framework for call graph construction in the presence of message sends and/or ...

14 Delivering a large information database

Christina L. Klein

February 1996 **Proceedings of the 13th annual international conference on Systems documentation: emerging from chaos: solutions for the growing complexity of our jobs**Full text available:  [pdf\(1.01 MB\)](#)Additional Information: [full citation](#), [index terms](#)**15 Reliability and security: Java cryptography on KVM and its performance and security optimization using HW/SW co-design techniques**


Yusuke Matsuoka, Patrick Schaumont, Kris Tiri, Ingrid Verbauwhede

September 2004 **Proceedings of the 2004 international conference on Compilers, architecture, and synthesis for embedded systems**Full text available:  [pdf\(188.08 KB\)](#)Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

This paper describes a design approach to include and optimize Java based cryptographic applications into resource limited embedded devices. For easy prototyping and to be platform independent, the security applications are first developed in Java. Two Java cryptographic libraries, the Bouncy Castle API and the IAIK API are ported to a real embedded device for cost and performance evaluation. It requires 0.88Mbytes to 1.2Mbytes in the KVM footprint size and a few milliseconds to run secret key al ...

Keywords: cryptography, design, embedded systems, java, security**16 Vortex: an optimizing compiler for object-oriented languages**

Jeffrey Dean, Greg DeFouw, David Grove, Vassily Litvinov, Craig Chambers

October 1996 **ACM SIGPLAN Notices , Proceedings of the 11th ACM SIGPLAN conference on Object-oriented programming, systems, languages, and applications, Volume 31 Issue 10**Full text available:  [pdf\(2.45 MB\)](#)Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)


Previously, techniques such as class hierarchy analysis and profile-guided receiver class

prediction have been demonstrated to greatly improve the performance of applications written in pure object-oriented languages, but the degree to which these results are transferable to applications written in hybrid languages has been unclear. In part to answer this question, we have developed the Vortex compiler infrastructure, a language-independent optimizing compiler for object-oriented languages, with ...

17 Role model designs and implementations with aspect-oriented programming

Elizabeth A. Kendall

October 1999 **ACM SIGPLAN Notices , Proceedings of the 14th ACM SIGPLAN conference on Object-oriented programming, systems, languages, and applications, Volume 34 Issue 10**

Full text available:  [pdf\(1.63 MB\)](#)

Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)


This paper describes research in applications of aspect-oriented programming (AOP) as captured in the AspectJ™ language. In particular, it compares object-oriented and aspect-oriented designs and implementations of role models. Sections 1, 2, and 3 provide background information on role models, object-oriented role model implementations, and aspect-oriented programming, respectively. New aspect-oriented designs for role models are explored in sections 4, 5, and 6. ...

Keywords: aspect-oriented programming, role modelling

18 Design considerations in language processing tools for Ada

W. Babich, L. Weissman, M. Wolfe

September 1982 **Proceedings of the 6th international conference on Software engineering**

Full text available:  [pdf\(581.45 KB\)](#)

Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)


The Ada Language System (ALS) is a complete programming environment for the development of Ada programs. This paper discusses the design objectives of those portions of the ALS which support translation and execution of Ada programs, particularly the compiler, linker, and program library. The ALS capabilities for maintenance of software configuration control are highlighted. Tradeoffs in the design of the compiler phase structure and intermediate languages are presented.

Keywords: Ada, Compiling, Configuration control, Programming environments

19 Finite-static code generation

Christopher W. Fraser, Todd A. Proebsting

May 1999 **ACM SIGPLAN Notices , Proceedings of the ACM SIGPLAN 1999 conference on Programming language design and implementation, Volume 34 Issue 5**


Full text available:  [pdf\(1.10 MB\)](#)

Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

This paper describes GBURG, which generates tiny, fast code generators based on finite-state machine pattern matching. The code generators translate postfix intermediate code into machine instructions in one pass (except, of course, for backpatching addresses). A stack-based virtual machine---known as the *Lean Virtual Machine* (LVM)---tuned for fast code generation is also described. GBURG translates the two-page LVM-to-x86 specification into a code generator that fits entirely in an 8 KB ...

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Sameer Shende, Allen D. Malony, Janice Cuny, Peter Beckman, Steve Karmesin, Kathleen Lindlan
August 1998 **Proceedings of the SIGMETRICS symposium on Parallel and distributed tools**

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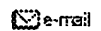
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